		1449A/PTO (		Complete if Known						
THUE	ORM	ATION	OSURE	Application Number 10/810		810,748				
0112	<u>Z</u>		10 4 N T	Filing Date 3/26/04		14				
STA	A3/EIM	ENTB	Y APPL	ICANT	First Named Inventor: Iu-Meng T		ng To	Гот Ho		
HIL 2 7 201	(use	as many shee	is as necessar	y)	Group Art Unit	•	2824			
	* / ·				Examiner Name		Not ye	Not yet assigned		
Boss Den			of	2	Attorney Docket	Number	02986	.P029	С	
				U.S. PATENT	DOCUMENTS					
Exam. Ci	te	U.S. Patent I	Document	Name of Pa	tentee or Applicant		of Publica		Pages, Column	
Initial* No	·.'	Number	Kind Code	of Cite	ed Document		ed Docum		Lines, Where Rele Passages or Rele	
			(Ifknown)						Figures Appea	
Ţ		723,908			nida, et al.		3/1998			
11	4	,353,040 US			nm, et al.		/5/1982 16/2001			
TT	200	1/0013422	A1	3	chaper	8/	10/2001			
	1200	170010122			<del></del>					
-									-	
										$\overline{\cdot}$
						<u> </u>				J
				7-12-1						_
			j	FOREIGN PATEN	T DOCUMENT	rs				
Exam. Cit	, —	reign Patent D		Name of Patente	e or Applicant	Date of Pub			ges, Columns,	T <sup>6</sup>
Initial* No.1 Office3 Number		Number <sup>4</sup>	Kind Code <sup>5</sup> of Cited		cument				es, Where Relevant	
		<del></del>	(If known)					Fi	gures Appear	
	-									
	<del>                                     </del>									
										<u> </u>
					4-76-71 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1				S	أ

1Mm

Examiner

Signature

**Date Considered** 

5/12/05

<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the twoletter code (WIPO Standard S.3). <sup>4</sup>For Japanese patent documents, the indication of the year of reign of the Emperor must precede the senal number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

OIFE	4											
11 21	2004 3											
Substitute for Felin 1449A/PTO (Modified)					Complete if Known							
Substitute for Febru 1449A/PTO (Modified)						Application Number 10/810,748						
				Filing Date	•	3/26/04						
STA		NT BY			NT	First Named I	nventor:		Iu-Meng Tom Ho			
	(use as r	many sheets a	s necess	ary)		Group Art Un	it		2824			
						Examiner Na	me.		Not yet assigned			
Sheet	2	O	f	2		Attorney Doc	ket Number		02986.P029	9C		
	=	(	OTHER	ART - I	NO PAT	ENT LITERAT	URE DOCU	MENT	s			
Examiner Initials*	Cite No <sup>1</sup>	Include na item (	ame of took, m	he author agazine, numb	(in CAP) journal, s er(s), pul	ITAL LETTERS), serial, symposium blisher, city and/o	title of the ar n, catalog, etc or country wh	ticle (w c.), date ere put	hen approp , page(s), v lished	rolume-iss	ue	T <sup>2</sup>
TT		May 2004			Report	for PCT Interna	ational Appli	n No. U	JS02/2426	7, mailed	1 10	
				<u>-</u>			. <u> </u>					
						<del>- \ </del>						
							·					
<del></del>												
				-								
								-				

	1		
Examiner Signature	hun book	Date Considered	5/12/05
Signature	TIWI WILL	Considered	01/2/2

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

<sup>\*</sup>Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>&</sup>lt;sup>1</sup>Unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)					Attorney Docket No.: 02986.P029C	Application Number:		
	.,				First Named Inventor: Iu-Meng Tom Ho			
					Filing Date:			
				U.S. PATEN	T DOCUMENTS			
Exam. Initial*	Cite No. <sup>1</sup>	U.S. Patent Document		1	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number	Kind Code <sup>2</sup> (If known)		· · · · · · · · · · · · · · · · · · ·			
TT		6,348,722		Yoshikoshi		02-19-2002		
3 1					<del> </del>			

		OTHER ART – NO PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation <sup>2</sup>
IJ		Magma Design Automation, Inc., "Deep-Submicron Signal Integrity", white paper, 2002	
TT		Andrey V. Mezhiba, Eby G. Friedman, "Scaling Trands of On-Chip Power Distribution Noise", SLIP'02, April 6-7, 2002, San Diego, California, USA, pp.47-53	
Π		Sani R. Nassif, Onsi Fakhouri, "Technology Trends in Power-Grid-Induced Noise", SLIP'02, April 6-7, 2002, San Diego, California, USA, pp.55-59	
TI		Seongkyun Shin, Yungseon Eo, William R. Eisenstadt, Jongin Shim, "Analytical Signal Integrity Verification Models for Inductance-Dominant Multi-Coupled VLSI Interconnects", SLIP'02, April 6-7, 2002, San Diego, California, USA, pp.61-68	
. 11		S. Khatri, A. Mehrotra, R. Brayton, A. Sangiovanni-Vincentelli, and R. Otten, "A novel VLSI layout fabric for deep sub-micron applications," in <i>Proceedings of the Design Automation Conference</i> , (New Orleans), June 1999.	
TT		Sunil P. Khatri, Robert K. Brayton, Alberto Sangiovanni-Vincentelli, "Cross-talk Immune VLSI Design using a Network of PLAs Embedded in a Regular Layout Fabric", IEEE/ACM International Conference on Computer Aided Design, ICCAD-2000, November 5-9, 2000, San Jose, CA, USA	
TT		Sunil P. Khatri, Robert K. Brayton, Alberto Sangiovanni-Vincentelli, "Cross-talk Noise Immune VLSI Design Using Regular Layout Fabrics", Kluwer Academic Publisher: Boston, 2001 (front cover, pages i-xix, 1-51, 95-112 and back cover).	

Examiner	11000	Colin	Date Considered	Chalos
Signature	1 Wen	MUL	li	3/12/03

<sup>\*</sup>Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>&</sup>lt;sup>1</sup>Unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.